



TRUTH TABLE

FS[0]	FS[1]	CTL	FA15	FA16	FA17	FA18
X	X	0	A15	A16	A17	A18
0	0	1	~A15	A16	A17	A18
1	0	1	A15	~A16	A17	A18
0	1	1	A15	A16	~A17	A18
1	1	1	A15	A16	A17	~A18

A prefix of "~" indicates the complement (inversion) of the signal.
 When CTL is LOW, A[18:15] are reflected on BA[18:15].
 When CTL is HIGH, FS[1:0] select the bit which will be inverted.

QuickLogic
 Corporation
 1277 Orleans Drive
 Sunnyvale, CA 94089

FLASHADR
 FLASHADR
 .
 8/23/2003 8:44:28 PM