



TRUTH TABLE

INPUT A	INPUT B	INPUT C	MAJ OUT	XNOR OUT
0	0	0	0	1
1	0	0	0	0
0	1	0	0	0
1	1	0	1	0
0	0	1	0	0
1	0	1	1	0
0	1	1	1	0
1	1	1	1	1

With no errors, all XNOR outputs will be HIGH and the NAND output will be LOW.
 The output data is corrected on a bit-by-bit basis, always reflecting the majority input state.

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